

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Attorney Docket Number	1011-54375-01
Application Number	09/620,021
Filing Date	July 20, 2000
First Named Inventor	Rajski
Art Unit	2133
Examiner Name	Phung M. Chung

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee

FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee
pmc		Japan	01-239486	9.25.1989	Masaaki (English abstract)
		Japan	03-002579	1.8.1991	Shuichi et al. (English abstract)
		Japan	05-215816	8.27.1993	Yasushi (English abstract)
pmc		Japan	11-030646	2.2.1999	Hideki (English abstract)

OTHER DOCUMENTS

Examiner's Initials*	Cite No. (optional)	

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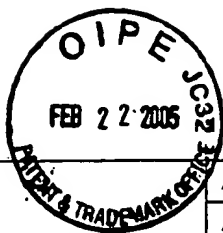
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pmc		Lew Yan Voon et al., "BIST Linear Generator based on Complemented Outputs," <i>IEEE VLSI Test Symp.</i> , pp. 137-142 (1992).			
pmc		Zacharia et al., "Two-Dimensional Test Data Decompressor for Multiple Scan Designs," <i>International Test Conference</i> , pp. 186-194 (1996).			

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gmc		Zacharia et al., "Two-Dimensional Test Data Decompressor for Multiple Scan Designs," <i>International Test Conference</i> , pp. 186-194 (1996).

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